

ABSTRACT OF THE DISCLOSURE

A simulator has a simulation CPU and a memory having a plurality of memory areas which include memory areas write-accessible from a control CPU connected to
5 the simulator and memory areas read-accessible from the simulation CPU. Control information written in the memory area by the control CPU is read out by the simulation CPU. The simulation CPU executes simulation on the basis of the control information and writes the
10 execution result of the simulation in the memory.